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Inventor

Giuseppe Curello et al.

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Title

Process for Producing a Doped Semiconductor Substrate

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Examiner

Igwe U. Anya

Customer No.:

24131

Hon. Commissioner for Patents Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97(b)(4)

Sir:

In accordance with 37 C.F.R. 1.97(b)(4), a copy of the following U.S. patent is submitted herewith:

U.S. Patent No. 5,296,388 (Kameyama et al.), dated March 22, 1994.

Respectfully submitted,

For Applicants

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